

1/11

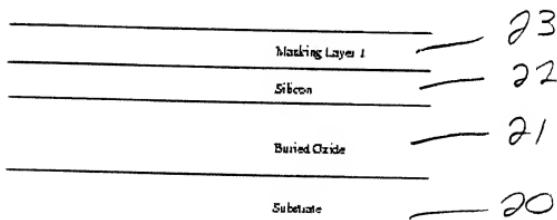
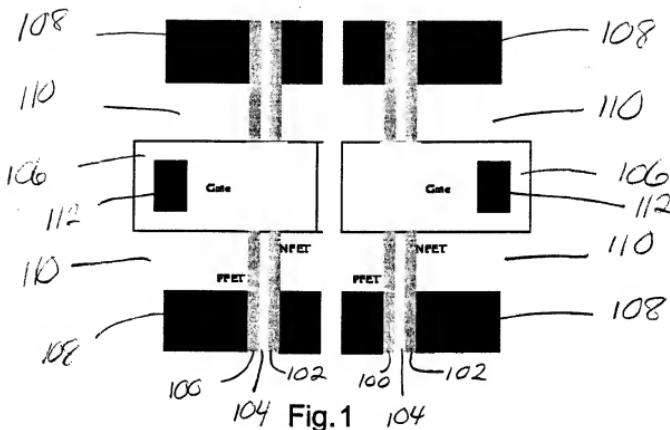


Fig.2

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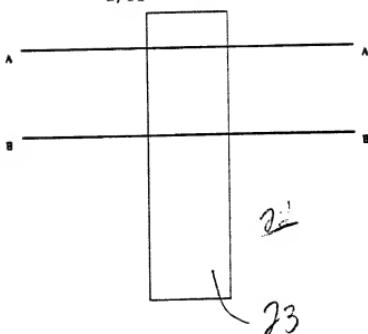


Fig.3

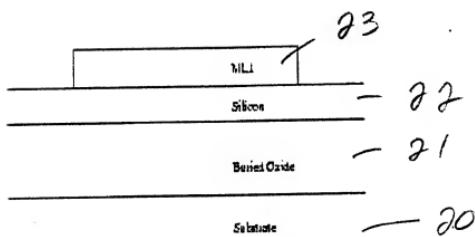


Fig.4

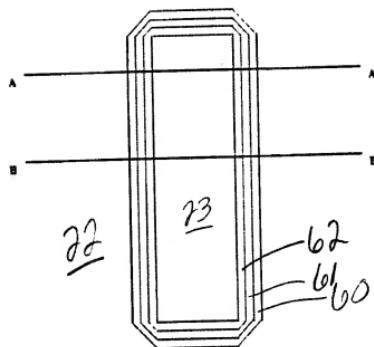


Fig.5

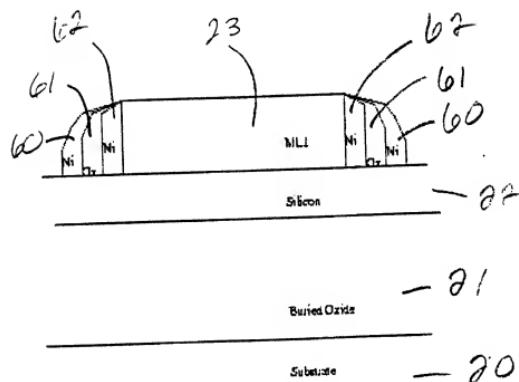


Fig.6

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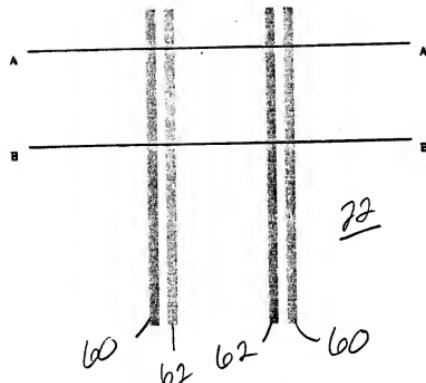


Fig. 7

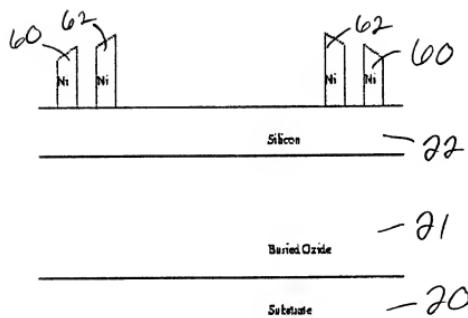


Fig. 8

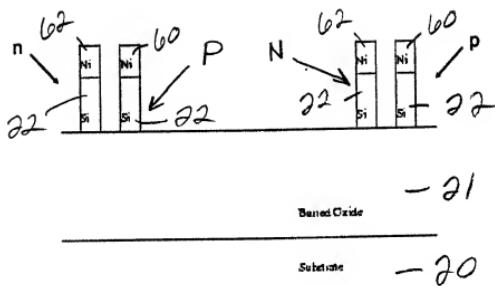


Fig.9

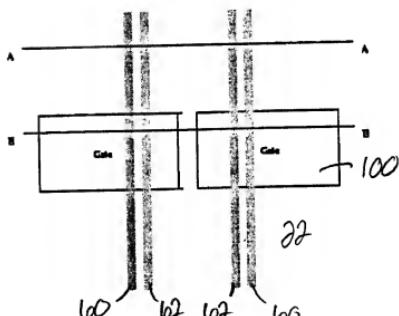
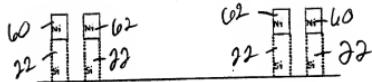


Fig.10

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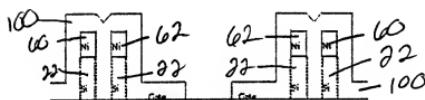
Series Gate

- 21

Subtract

- 20

Fig. 11



Series Gate

- 21

Subtract

- 20

Fig. 12

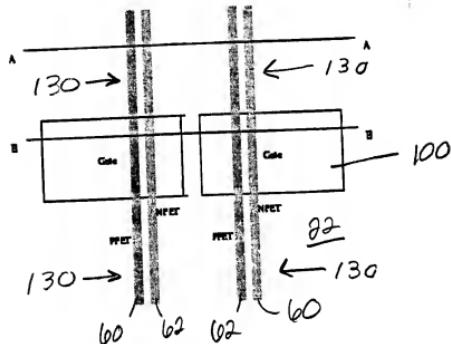
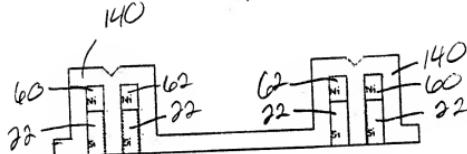


Fig. 13



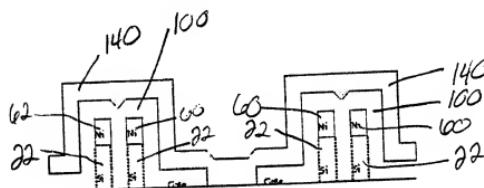
-21

Buried Oxide

Substrate

-20

Fig.14



-21

Buried Oxide

Substrate

-20

Fig.15

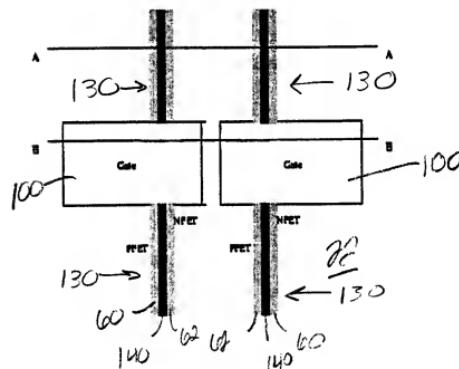


Fig.16

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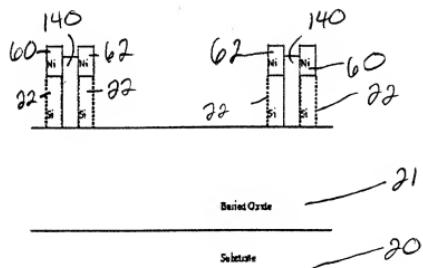


Fig. 17

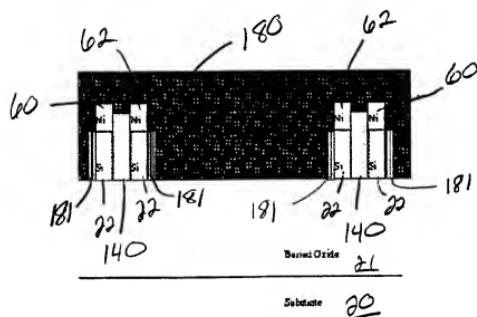


Fig. 18

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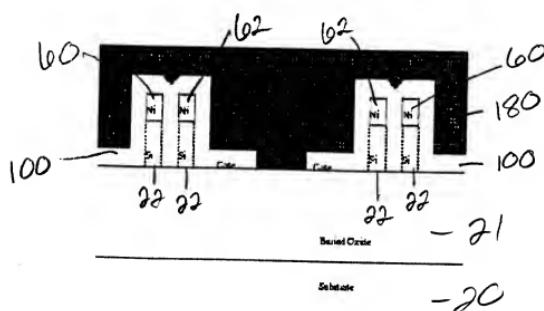


Fig. 19

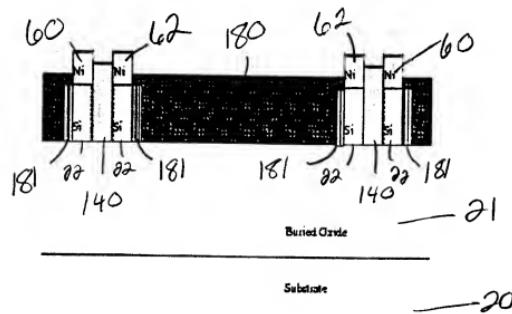


Fig. 20

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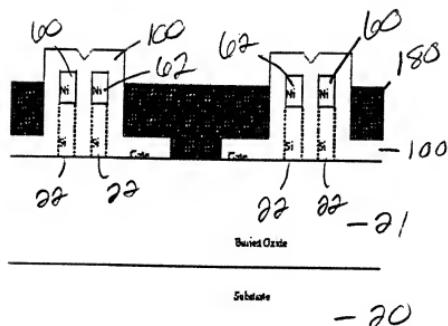


Fig.21

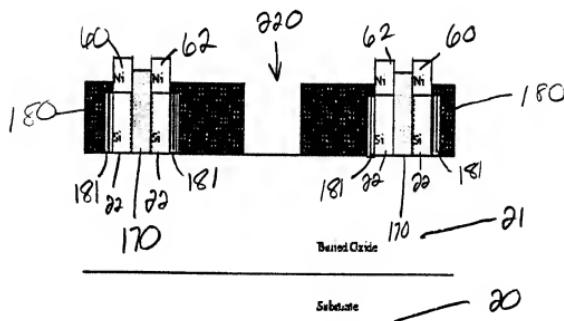


Fig.22

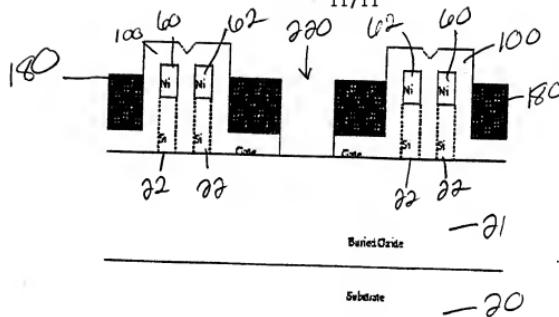


Fig.23

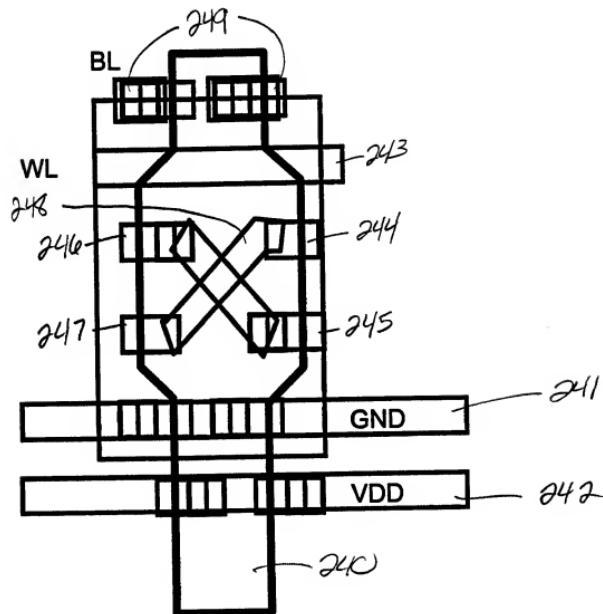


Fig. 24